

FIG. 3

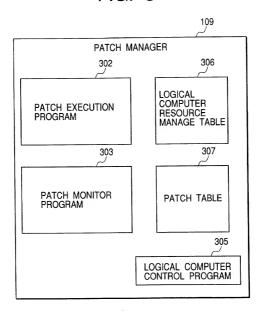
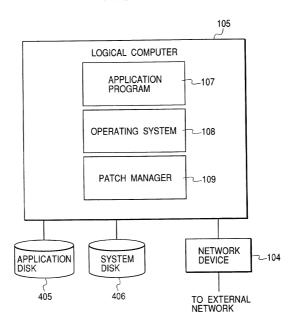


FIG. 4



306: LOGICAL COMPUTER RESOURCE MANAGE TABLE					
501	502 〈	503 \	504 \	505 \	
LOGICAL COMPUTER ID	PROCESSOR/ ASSIGN RATE	MEMORY	DISK DEVICE	NETWORK DEVICE	
LOGICAL COMPUTER #1	CPU1/100%	128MB	DISK 1 DISK 2	NET 1	
LOGICAL COMPUTER #2	CPU2/100%	128MB	DISK 3 DISK 4	NET 2	
LOGICAL COMPUTER #3	CPU3/50%	64MB	DISK 5		
LOGICAL COMPUTER #4	CPU3/50%	64MB	DISK 6	NET 3	

PATCH #1	<u> </u>
PATCH #3 OF FUNCTION COHHECTION NEXT	
PATCH #2	
PATCH #3 NOT YET	
17.1017 #0	×
PATCH #4 () 1999/3/26	×
PATCH #5 O 1999/8/15	
PATCH #6 1999/9/15	
PATCH #7 O NOT YET	0

FIG. 7

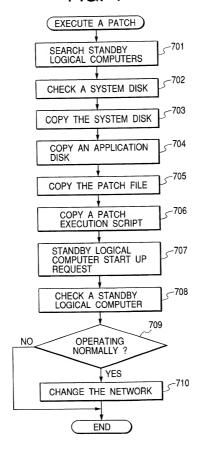


FIG. 8

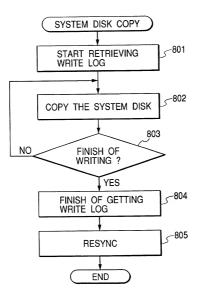


FIG. 9

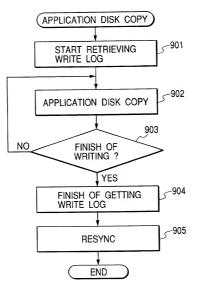


FIG. 10

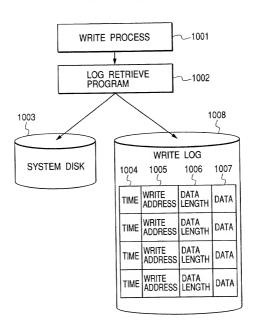
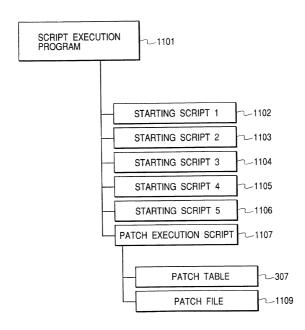


FIG. 11



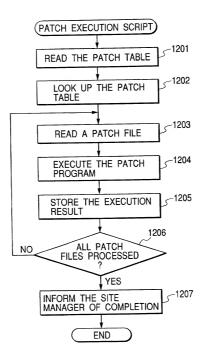
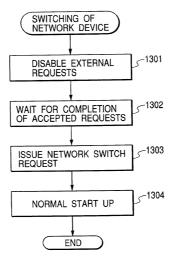
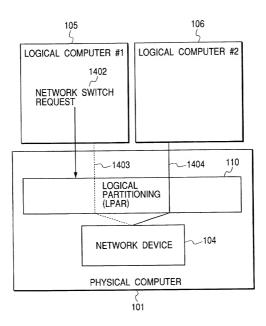
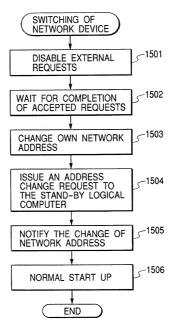
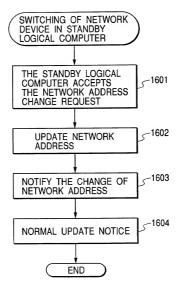


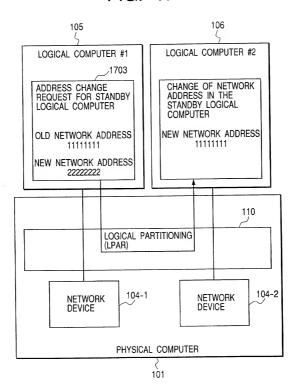
FIG. 13

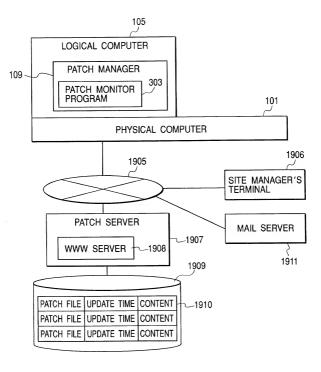












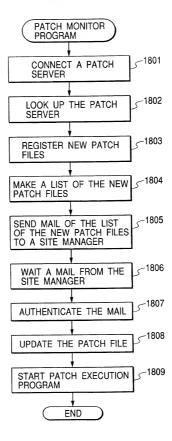


FIG. 20

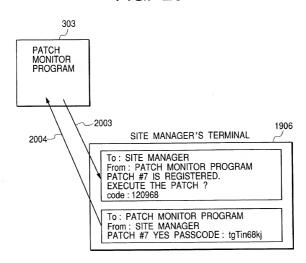
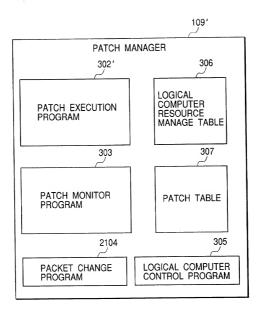


FIG. 21



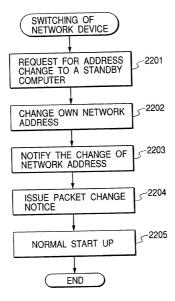


FIG. 23

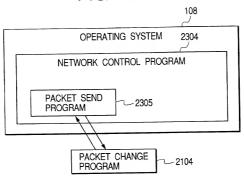


FIG. 24

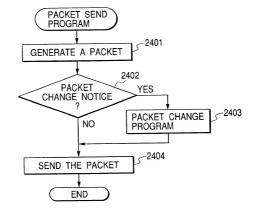


FIG. 25

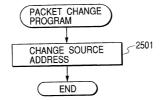


FIG. 26A



FIG. 26B

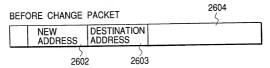


FIG. 26C

